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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Complete if Known

Application Number	10/507,449
Filing Date	September 13, 2004
First Named Inventor	Fletcher
Art Unit	Unknown
Examiner Name	Not Assigned
Attorney Docket Number	04-748

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U. S. PATENT DOCUMENTS

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FOREIGN PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	T ⁶
		Country Code ³ Number ⁴ Kind Code ⁵ (if known)	MM-DD-YYYY			
DV		GB 2,188,782	10-7-87	STC PLC		
↓		GB 2,219,471	12-13-89	Cossor		
		GB 2,357,842	7-4-01	Furano		
		GB 2,372,378	8-21-02	Roke Manor		
		JP 580201082	11-22-83	Hitachi		
		JP 0802797980	10-22-96	Nippon		

Examiner Signature	/Don Vo/	Date Considered	06/02/2006
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of

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NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
DV		Scott, et al., "A Sparse Approach in Partially Adaptive Linearly Constrained Arrays", IEEE, pp. 541-544 (1994)	
DV		Godara, et al. "Application of Antenna Arrays to Mobile Communications, Part II: Beam-Forming and Direction of Arrival Considerations", IEEE, pp. 1195-1245 (1997)	
DV		Bell, et al., "Adaptive Nulling for Multiple Desired Signals Based on Signal Waveform Estimation", IEEE, pp. 919-923 (1992)	

Examiner Signature	/Don Vo/	Date Considered	06/02/2006
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